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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): S. NARUI et al
Serial No.: 10/642,743
Filed: August 19, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND METHOD FOR MANUFACTURING THE SAME
Group: 2826
Examiner: K. QUINTO

AMENDMENT

MS: AMENDMENTS

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

March 7, 2005

Sir:

In response to the Office Action dated September 8, 2004, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendment to the Title;

Amendments to the Claims, and

Remarks are included following the Amendment.

AMENDMENT TO THE TITLE

Please amend the title as follows:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING MEMORY
CELL SECTION HAVING CAPACITOR OVER BITLINE STRUCTURE AND WITH
THE MEMORY AND PERIPHERAL SECTIONS HAVING CONTACT PLUG
STRUCTURES CONTAINING A BARRIER FILM AND EFFECTING ELECTRICAL
CONTACT WITH MISFETS OF BOTH MEMORY AND PERIPHERAL SECTIONS
~~AND METHOD FOR MANUFACTURING THE SAME~~